

Amendments to the Specification

Kindly amend the paragraph at page 1, lines 4-5, as follows:

This [[Application Claims Priority From Provisional Application no. 60/164,981,]] application is a continuation of U.S. Application No. 09/712,410, filed November 13, 2000, ^{now Patent No. 6,696,892} which is incorporated herein by reference in its entirety, and which claims the benefit of U.S. Provisional Application No. 60/164,981, filed November 11, 1999.

Kindly amend the paragraph at page 1, lines 8-10, as follows:

The present invention in general encompasses the field of analog integrated circuit design, and in particular [[embodiments]] embodiments, analog circuit design of programmable [[game]] gain amplifiers in low power supply environments.

Kindly amend the paragraph at page 2, lines 5-6, as follows:

FIG. 7 is a graphical illustration of a multiple switch (and multiple tap) programmable gain [[attenuators]] attenuator (PGA).

Kindly amend the paragraph at page 2, lines 26-27, as follows:

FIG. 19 is a schematic diagram of a HPGA [[in]] illustrating the switching device circuitry according to an embodiment of the invention.